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2/5/1

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010249833 **Image available**

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Solid state video camera exposure control system - incorporates screen brightness compensation path in which divider circuit is provided which divides input signal and signal from adder into predetermined value

Patent Assignee: MITSUBISHI ELECTRIC CORP (MITQ)

Number of Countries: 001 Number of Patents: 001

Patent Family:

| Patent No | Kind | Date | Applicat No | Kind | Date | Week |
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| JP 7075023 | A | 19950317 | JP 93220068 | A | 19930903 | 199520 B |

Priority Applications (No Type Date): JP 93220068 A 19930903

Patent Details:

| Patent No | Kind | Lan Pg | Main IPC | Filing Notes |
|------------|------|--------|--------------|--------------|
| JP 7075023 | A | 7 | H04N-005/335 | |

Abstract (Basic): JP 7075023 A

The solid state image pick-up has a light detection element (3) provided with a light receiving surface on which an input light is condensed. The output of the light detection element is fed independently and in parallel to the preamplifiers (6, 7), A/D converters (8, 9) and an offset compensation part (10, 11). The optical system consists of a relay type optical system (25) which in turn includes slit like opening part (26) at the first image information surface. After the offset compensation, screen brightness difference compensation parts (28, 29) is provided which consists of a division circuit. This division circuit divides the input image signal at a signal from an addition circuit into a predetermined value. The addition circuit adds the output image signal in predetermined pixel signal from a pixel selector. The control of this pixel selector is done by switching a switch for screen brightness difference compensation.

ADVANTAGE - Maintains slit opening part at optimum position. Judges horizontal number of selection of pixel domain. Avoids change of projection image with time of offset compensation data. Enables acquisition of data for offset compensation set by temperature.

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Title Terms: SOLID; STATE; VIDEO; CAMERA; EXPOSE; CONTROL; SYSTEM; INCORPORATE; SCREEN; BRIGHT; COMPENSATE; PATH; DIVIDE; CIRCUIT; DIVIDE; INPUT; SIGNAL; SIGNAL; ADDER; PREDETERMINED; VALUE

Derwent Class: W04

International Patent Class (Main): H04N-005/335

International Patent Class (Additional): H04N-001/04

File Segment: EPI

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